

**BP3318**
**P-CHANNEL ENHANCEMENT MODE POWER MOSFET**

BV <sub>DSS</sub>	-30V
R <sub>DS(ON)</sub>	50mΩ
I <sub>D</sub>	-20A

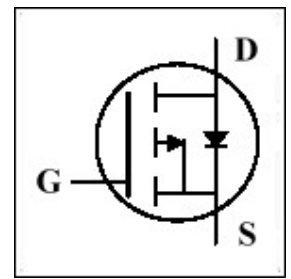
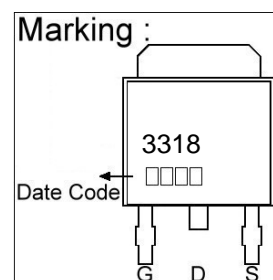
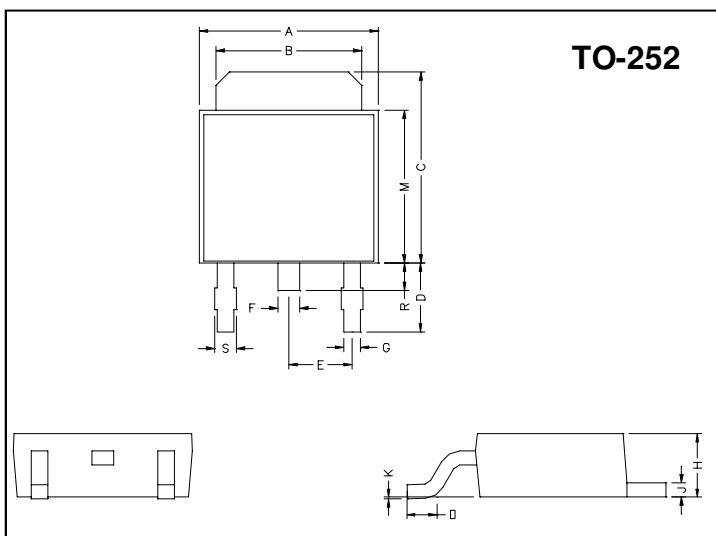
**Description**

The BP3318 utilized advanced processing techniques to achieve the lowest possible on-resistance, extremely efficient and cost-effectiveness device.

The TO-252 package is universally used for commercial-industrial applications.

**Features**

- \*Simple Drive Requirement
- \*Lower Gate Charge
- \*Fast Switching

**Package Dimensions**


REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	6.40	6.80	G	0.50	0.70
B	5.20	5.50	H	2.20	2.40
C	6.80	7.20	J	0.45	0.55
D	2.40	3.00	K	0	0.15
E	2.30 REF.		L	0.90	1.50
F	0.70	0.90	M	5.40	5.80
S	0.60	0.90	R	0.80	1.20

**Absolute Maximum Ratings**

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V <sub>DS</sub>	-30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub> @T <sub>C</sub> =25°C	-20	A
Continuous Drain Current	I <sub>D</sub> @T <sub>C</sub> =100°C	-13	A
Pulsed Drain Current <sup>1</sup>	I <sub>DM</sub>	-72	A
Total Power Dissipation	P <sub>D</sub> @T <sub>C</sub> =25°C	31	W
Linear Derating Factor		0.25	W/°C
Operating Junction and Storage Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	-55 ~ +150	°C

**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-case Max.	R <sub>thj-case</sub>	4.0	°C/W
Thermal Resistance Junction-ambient Max.	R <sub>thj-amb</sub>	110	°C/W

## Electrical Characteristics (T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	-	-	V	V <sub>GS</sub> =0, I <sub>D</sub> =-250uA
Breakdown Voltage Temperature Coefficient	ΔBV <sub>DSS</sub> /ΔT <sub>j</sub>	-	-0.1	-	V/°C	Reference to 25°C, I <sub>D</sub> =-1mA
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1.0	-	-3.0	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA
Forward Transconductance	g <sub>fs</sub>	-	9.6	-	S	V <sub>DS</sub> =-10V, I <sub>D</sub> =-10A
Gate-Source Leakage Current	I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> = ±20V
Drain-Source Leakage Current(T <sub>j</sub> =25°C)	I <sub>DSS</sub>	-	-	-1	uA	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0
Drain-Source Leakage Current(T <sub>j</sub> =70°C)		-	-	-25	uA	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0
Static Drain-Source On-Resistance <sup>2</sup>	R <sub>DS(ON)</sub>	-	-	50	mΩ	V <sub>GS</sub> =-10V, I <sub>D</sub> =-10A
		-	-	90		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5A
Total Gate Charge <sup>2</sup>	Q <sub>g</sub>	-	10	16	nC	I <sub>D</sub> =-10A V <sub>DS</sub> =-24V V <sub>GS</sub> =-4.5V
Gate-Source Charge	Q <sub>gs</sub>	-	3	-		
Gate-Drain ("Miller") Charge	Q <sub>gd</sub>	-	5	-		
Turn-on Delay Time <sup>2</sup>	T <sub>d(on)</sub>	-	9.6	-	ns	V <sub>DS</sub> =-15V I <sub>D</sub> =-10A V <sub>GS</sub> =-10V R <sub>G</sub> =3.3 R <sub>D</sub> =1.5
Rise Time	T <sub>r</sub>	-	18	-		
Turn-off Delay Time	T <sub>d(off)</sub>	-	19	-		
Fall Time	T <sub>f</sub>	-	14	-		
Input Capacitance	C <sub>iss</sub>	-	463	740	pF	V <sub>GS</sub> =0V V <sub>DS</sub> =-25V f=1.0MHz
Output Capacitance	C <sub>oss</sub>	-	187	-		
Reverse Transfer Capacitance	C <sub>rss</sub>	-	140	-		

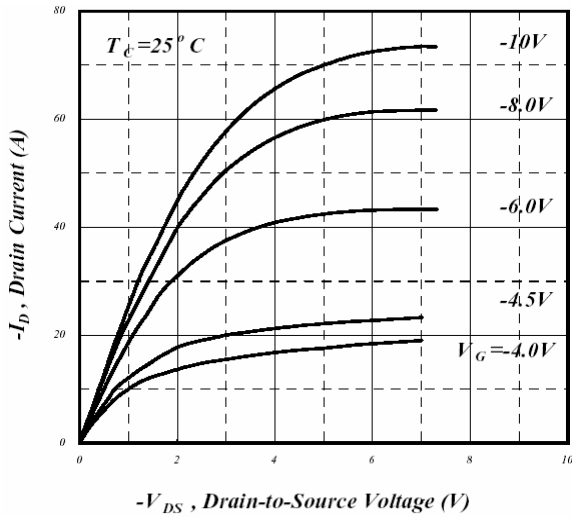
## Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage <sup>2</sup>	V <sub>SD</sub>	-	-	-1.2	V	I <sub>S</sub> =-10A, V <sub>GS</sub> =0V
Reverse Recovery Time	T <sub>rr</sub>	-	34	-	ns	I <sub>S</sub> =-10A, V <sub>GS</sub> =0V di/dt=100A/μs
Reverse Recovery Charge	Q <sub>rr</sub>	-	30	-	nC	

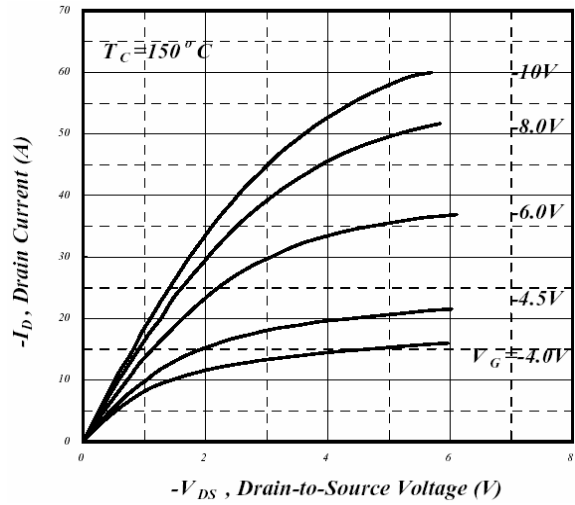
Notes: 1. Pulse width limited by Max. junction temperature.

2. Pulse width ≤ 300us, duty cycle ≤ 2%.

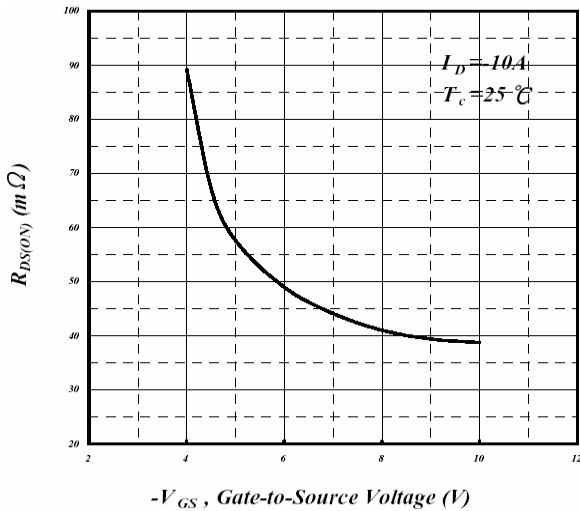
## Characteristics Curve



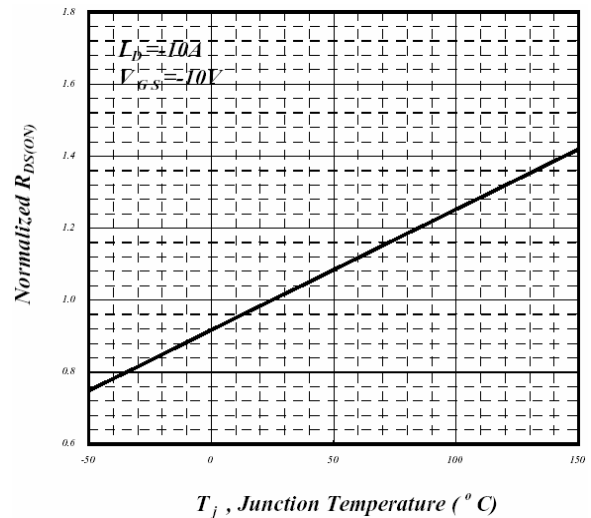
**Fig 1. Typical Output Characteristics**



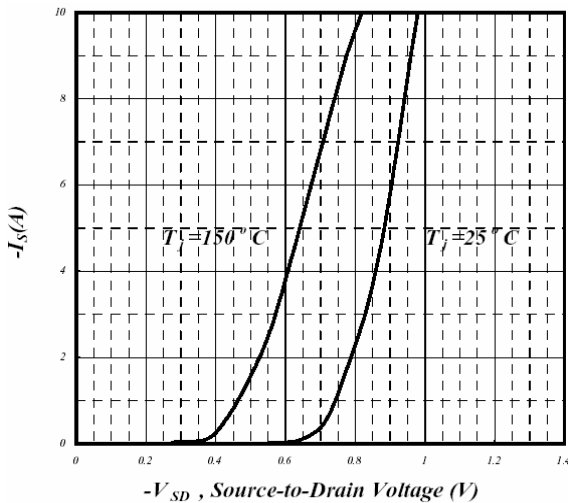
**Fig 2. Typical Output Characteristics**



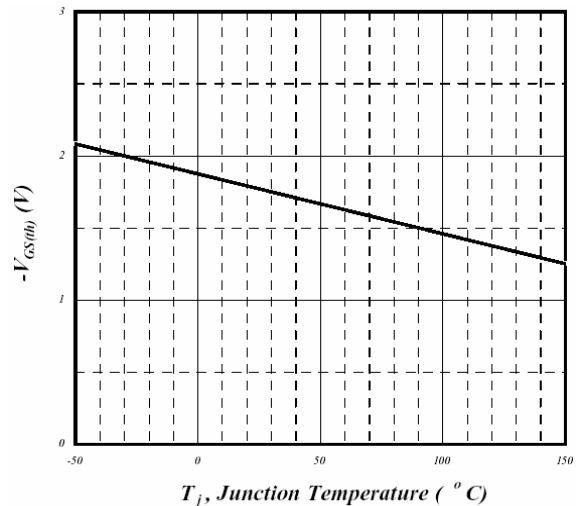
**Fig 3. On-Resistance v.s. Gate Voltage**



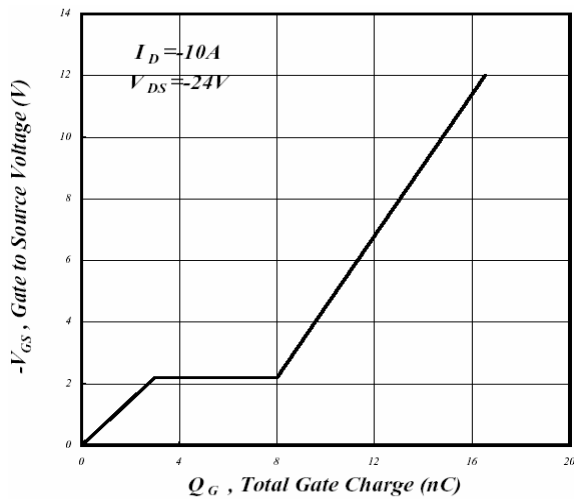
**Fig 4. Normalized On-Resistance v.s. Junction Temperature**



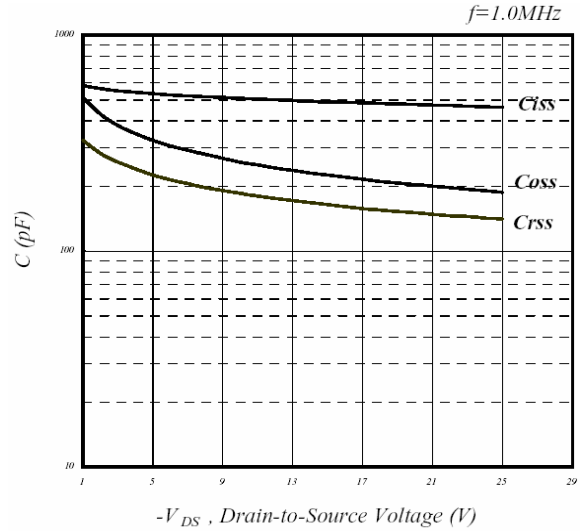
**Fig 5. Forward Characteristics of Reverse Diode**



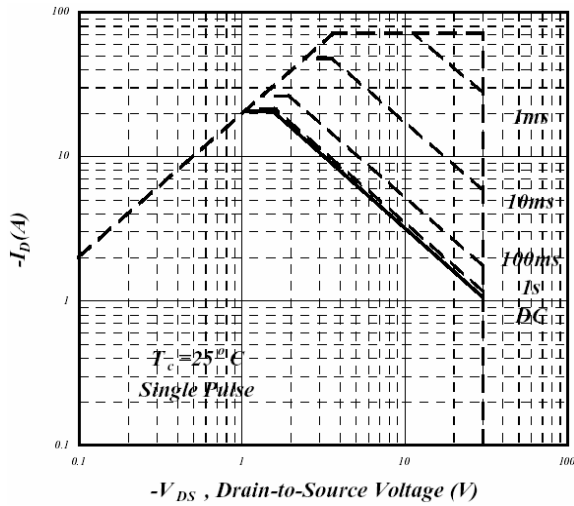
**Fig 6. Gate Threshold Voltage v.s. Junction Temperature**



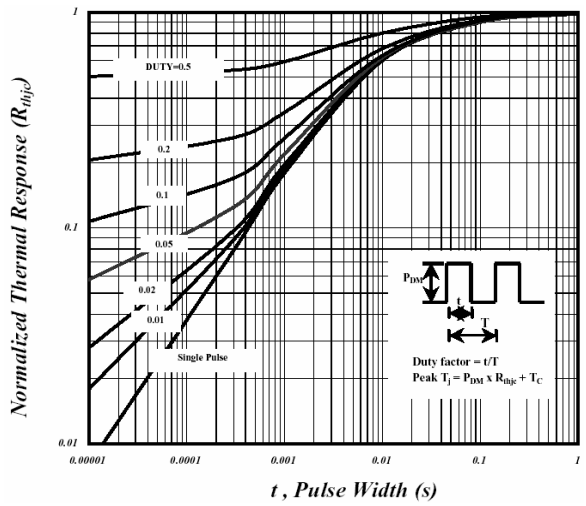
**Fig 7. Gate Charge Characteristics**



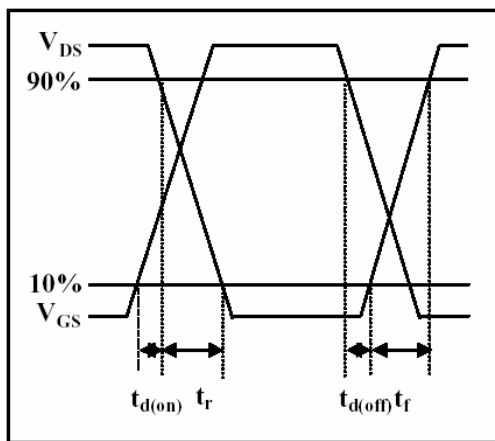
**Fig 8. Typical Capacitance Characteristics**



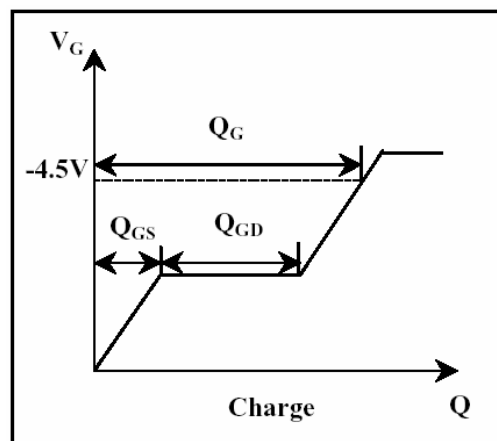
**Fig 9. Maximum Safe Operating Area**



**Fig 10. Effective Transient Thermal Impedance**



**Fig 11. Switching Time Waveform**



**Fig 12. Gate Charge Waveform**