

| | |
|---------------------|------|
| BV _{DSS} | -30V |
| R _{DS(ON)} | 26mΩ |
| I _D | -28A |

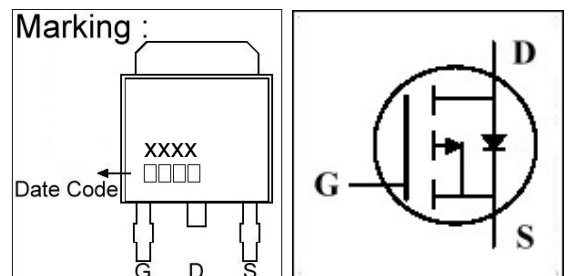
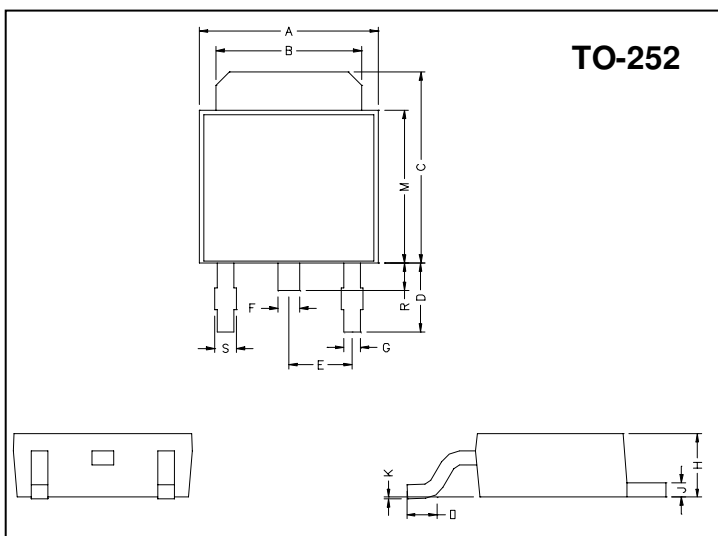
P-CHANNEL ENHANCEMENT MODE POWER MOSFET
Description

The BP3301 utilized advanced processing techniques to achieve the lowest possible on-resistance, extremely efficient and cost-effectiveness device.

The TO-252 package is universally used for commercial-industrial applications.

Features

- *Simple Drive Requirement
- *Lower Gate Charge
- *Fast Switching

Package Dimensions


| REF. | Millimeter | | REF. | Millimeter | |
|------|------------|------|------|------------|------|
| | Min. | Max. | | Min. | Max. |
| A | 6.40 | 6.80 | G | 0.50 | 0.70 |
| B | 5.20 | 5.50 | H | 2.20 | 2.40 |
| C | 6.80 | 7.20 | J | 0.45 | 0.55 |
| D | 2.40 | 3.00 | K | 0 | 0.15 |
| E | 2.30 REF. | | L | 0.90 | 1.50 |
| F | 0.70 | 0.90 | M | 5.40 | 5.80 |
| S | 0.60 | 0.90 | R | 0.80 | 1.20 |

Absolute Maximum Ratings

| Parameter | Symbol | Ratings | Unit |
|--|---------------------------------------|------------|------|
| Drain-Source Voltage | V _{DS} | -30 | V |
| Gate-Source Voltage | V _{GS} | ±20 | V |
| Continuous Drain Current | I _D @T _C =25°C | -28 | A |
| Continuous Drain Current | I _D @T _C =100°C | -13 | A |
| Pulsed Drain Current ¹ | I _{DM} | -72 | A |
| Total Power Dissipation | P _D @T _C =25°C | 31 | W |
| Linear Derating Factor | | 0.25 | W/°C |
| Operating Junction and Storage Temperature Range | T _j , T _{stg} | -55 ~ +150 | °C |

Thermal Data

| Parameter | Symbol | Value | Unit |
|--|-----------------------|-------|------|
| Thermal Resistance Junction-case Max. | R _{thj-case} | 4.0 | °C/W |
| Thermal Resistance Junction-ambient Max. | R _{thj-amb} | 110 | °C/W |

Electrical Characteristics (T_j = 25°C unless otherwise specified)

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Test Conditions |
|--|-------------------------------------|------|------|------|------|--|
| Drain-Source Breakdown Voltage | BV _{DSS} | -30 | - | - | V | V _{GS} =0, I _D =-250uA |
| Breakdown Voltage Temperature Coefficient | ΔBV _{DSS} /ΔT _j | - | -0.1 | - | V/°C | Reference to 25°C, I _D =-1mA |
| Gate Threshold Voltage | V _{GS(th)} | -1.0 | - | -3.0 | V | V _{DS} =V _{GS} , I _D =-250uA |
| Forward Transconductance | g _{fs} | - | 9.6 | - | S | V _{DS} =-10V, I _D =-10A |
| Gate-Source Leakage Current | I _{GSS} | - | - | ±100 | nA | V _{GS} = ±20V |
| Drain-Source Leakage Current(T _j =25°C) | I _{DSS} | - | - | -1 | uA | V _{DS} =-30V, V _{GS} =0 |
| Drain-Source Leakage Current(T _j =70°C) | | - | - | -25 | uA | V _{DS} =-24V, V _{GS} =0 |
| Static Drain-Source On-Resistance ² | R _{DS(ON)} | - | - | 26 | m | V _{GS} =-10V, I _D =-10A |
| | | - | - | 38 | | V _{GS} =-4.5V, I _D =-5A |
| Total Gate Charge ² | Q _g | - | 10 | 16 | nC | I _D =-10A V _{DS} =-24V V _{GS} =-4.5V |
| Gate-Source Charge | Q _{gs} | - | 3 | - | | |
| Gate-Drain ("Miller") Charge | Q _{gd} | - | 5 | - | | |
| Turn-on Delay Time ² | T _{d(on)} | - | 9.6 | - | ns | V _{DS} =-15V I _D =-10A V _{GS} =-10V R _G =3.3 R _D =1.5 |
| Rise Time | T _r | - | 18 | - | | |
| Turn-off Delay Time | T _{d(off)} | - | 19 | - | | |
| Fall Time | T _f | - | 14 | - | | |
| Input Capacitance | C _{iss} | - | 463 | 740 | pF | V _{GS} =0V V _{DS} =-25V f=1.0MHz |
| Output Capacitance | C _{oss} | - | 187 | - | | |
| Reverse Transfer Capacitance | C _{rss} | - | 140 | - | | |

Source-Drain Diode

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Test Conditions |
|---------------------------------|-----------------|------|------|------|------|--|
| Forward On Voltage ² | V _{SD} | - | - | -1.2 | V | I _S =-10A, V _{GS} =0V |
| Reverse Recovery Time | T _{rr} | - | 34 | - | ns | I _S =-10A, V _{GS} =0V di/dt=100A/μs |
| Reverse Recovery Charge | Q _{rr} | - | 30 | - | nC | |

Notes: 1. Pulse width limited by Max. junction temperature.

2. Pulse width ≤ 300us, duty cycle ≤ 2%.

Characteristics Curve

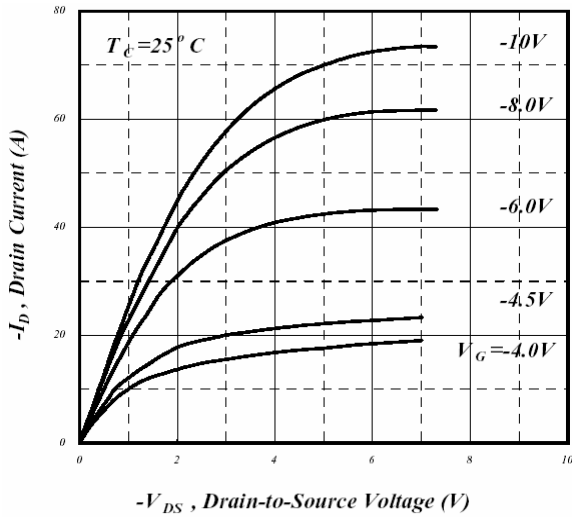


Fig 1. Typical Output Characteristics

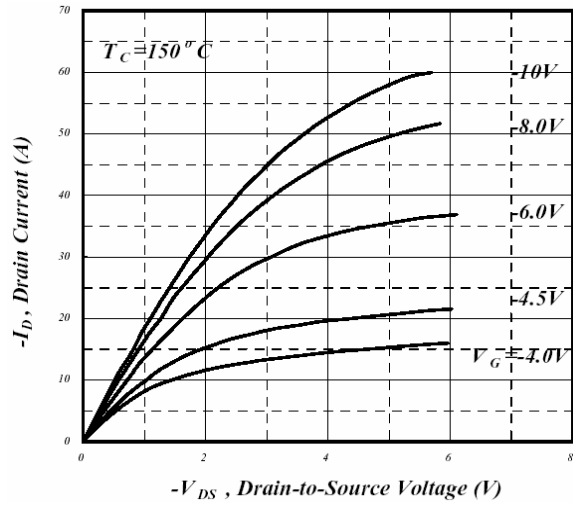


Fig 2. Typical Output Characteristics

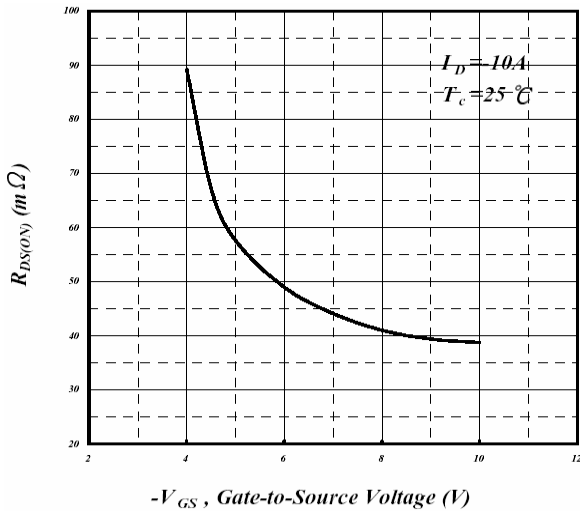


Fig 3. On-Resistance v.s. Gate Voltage

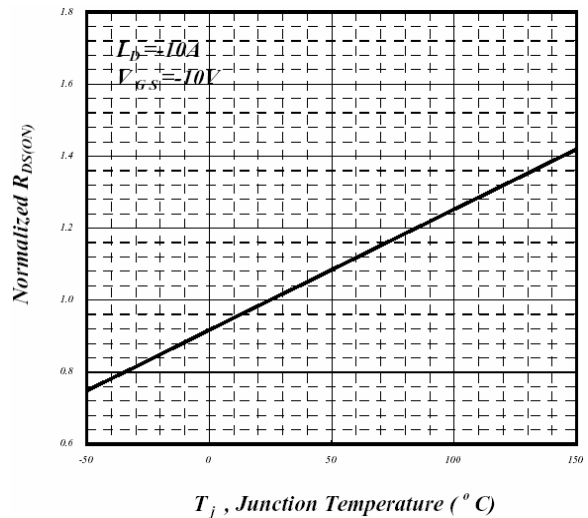


Fig 4. Normalized On-Resistance v.s. Junction Temperature

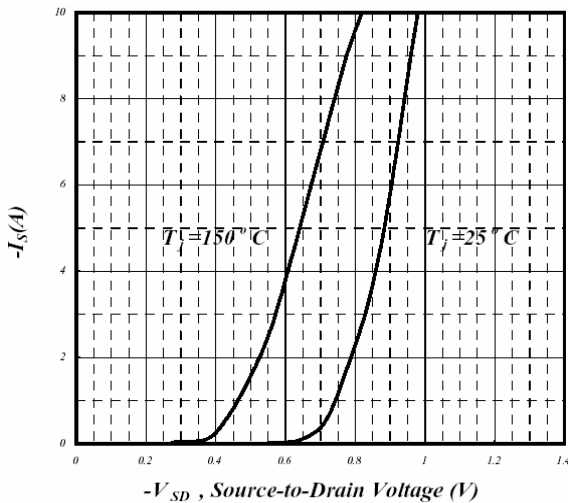


Fig 5. Forward Characteristics of Reverse Diode

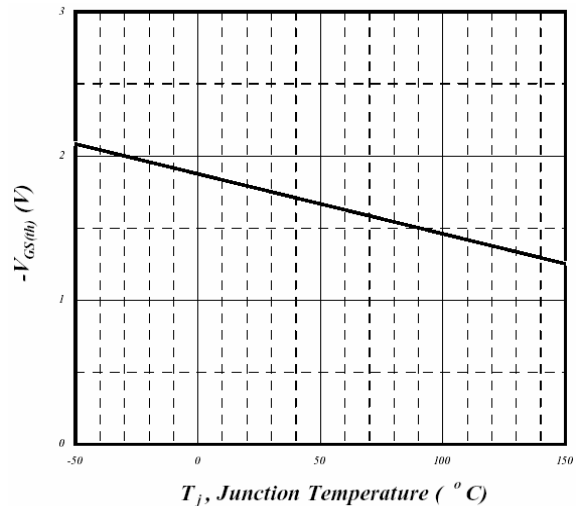


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

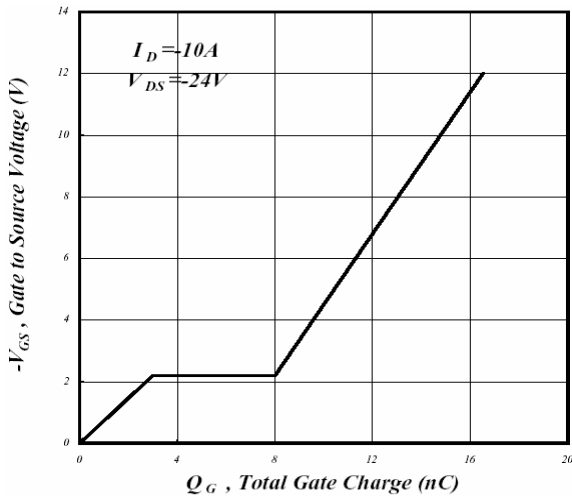


Fig 7. Gate Charge Characteristics

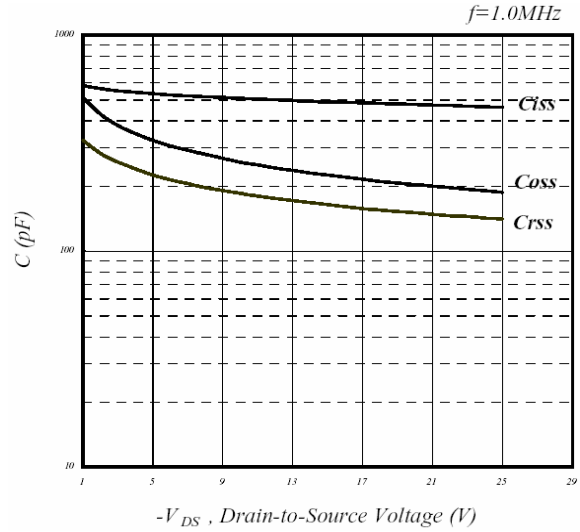


Fig 8. Typical Capacitance Characteristics

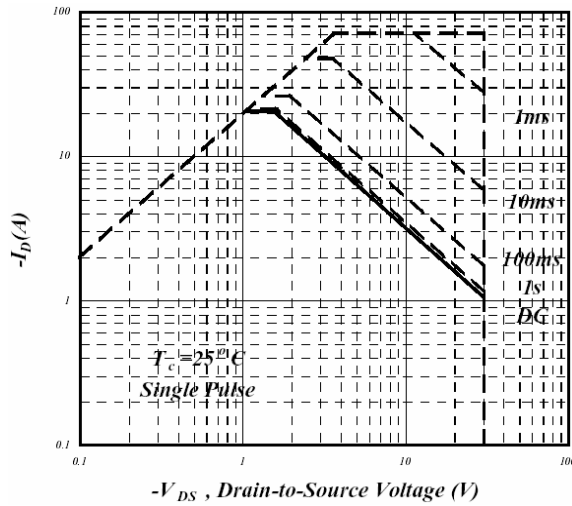


Fig 9. Maximum Safe Operating Area

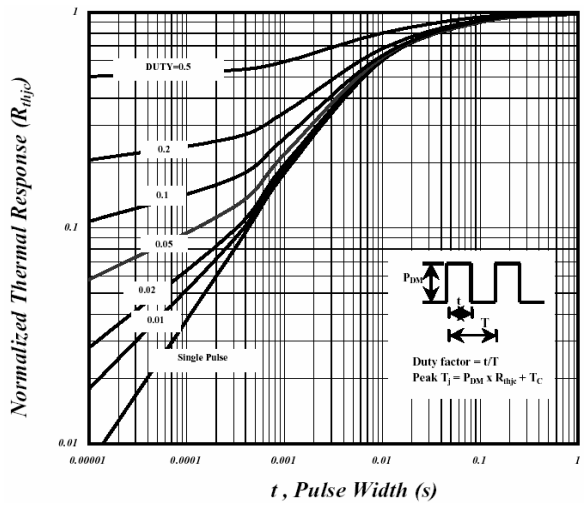


Fig 10. Effective Transient Thermal Impedance

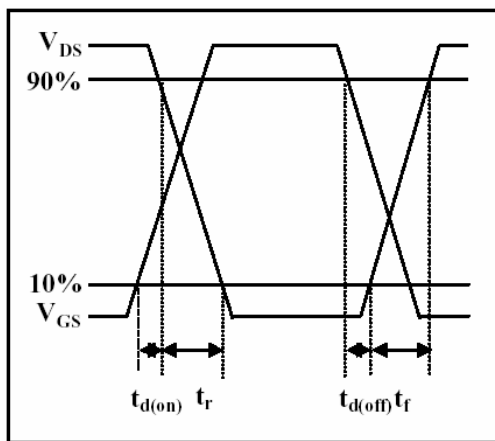


Fig 11. Switching Time Waveform

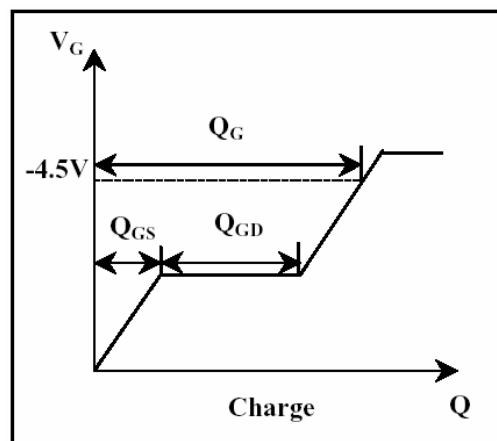


Fig 12. Gate Charge Waveform