

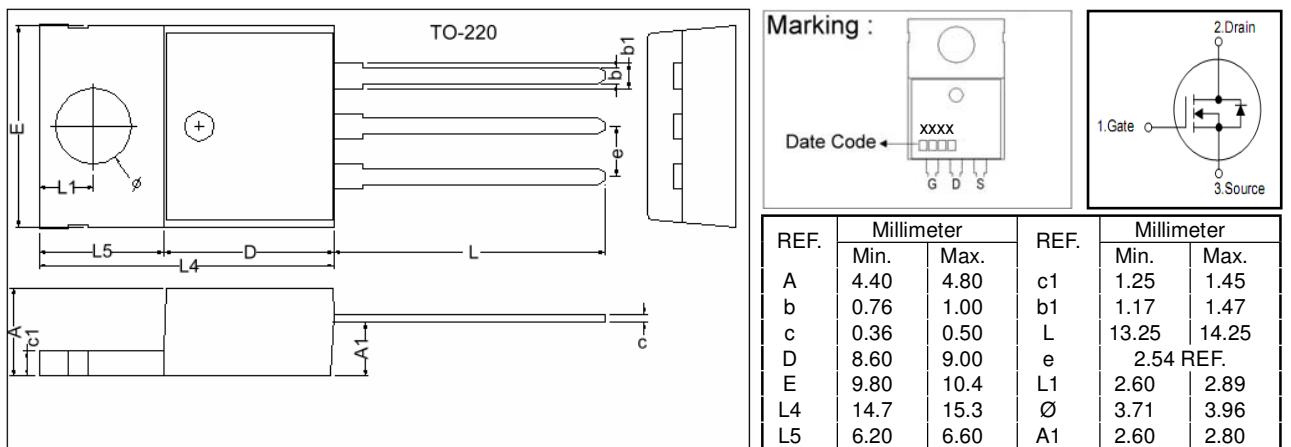
N-Channel MOSFET

BVDSS	600V
RDS(ON)	0.8Ω
ID	7.4A

Features

- * Ultra low gate charge (typical 29nC)
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness
- * Lower R_{DS(ON)}

Package Dimensions



Absolute maximum ratings (T_C=25°C , unless otherwise specified)

Parameter	Symbol	Rating	Units
Drain-source voltage	V _{DSS}	600	V
Gate-source voltage	V _{GSS}	±30	V
Drain current continuous	I _D	7.4	A
T _C =100°C		4.7	A
Drain current pulsed (note1)	I _{DP}	29.6	A
Avalanche energy	E _{AR}	14.2	mJ
Single Pulse (note2)	E _{AS}	580	mJ
Peak diode recovery dv/dt (note3)	dv/dt	4.5	V/ns
Total power dissipation	P _D	142	W
T _C =25°C		1.14	W/°C
Derate above 25°C			
Junction temperature	T _J	+150	°C
Storage temperature	T _{STG}	-55~+150	°C

Thermal characteristics

Parameter	Symbol	Rating	Unit
Thermal resistance junction-ambient	R _{thJA}	62.5	°C/W
Thermal resistance,case-to-sink typ.	R _{thCS}	0.5	
Thermal resistance junction-case	R _{thJC}	0.88	

Electrical characteristics ($T_J=25^\circ\text{C}$, unless otherwise notes)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units	
Off characteristics							
Drain-source breakdown voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	600	-	-	V	
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}}=600\text{V}, V_{\text{GS}}=0\text{V}$	-	-	10	μA	
		$V_{\text{DS}}=480\text{V}, T_c=125^\circ\text{C}$	-	-	100	μA	
Gate-body leakage current	Forward	I_{GSS}	$V_{\text{GS}}=30\text{V}, V_{\text{DS}}=0\text{V}$	-	-	100	nA
	Reverse		$V_{\text{GS}}=-30\text{V}, V_{\text{DS}}=0\text{V}$	-	-	-100	nA
Breakdown voltage temperature coefficient	$\Delta \text{BV}_{\text{DSS}} \Delta T_J$	$I_{\text{D}}=250\mu\text{A}$	-	0.67	-	V/ $^\circ\text{C}$	
On characteristics							
Gate threshold voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	3.0	-	5.0	V	
Static drain-source on-resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=3.7\text{A}$	-	0.8	1.0	Ω	
Dynamic characteristics							
Input capacitance	C_{ISS}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	1100	1430	pF	
Output capacitance	C_{OSS}		-	135	175	pF	
Reverse transfer capacitance	C_{RSS}		-	16	21	pF	
Switching characteristics							
Turn-on delay time	$t_{\text{D}(\text{ON})}$	$V_{\text{DD}}=300\text{V}, R_{\text{G}}=25\Omega, I_{\text{D}}=7.4\text{A}$ (note 4,5)	-	-	70	ns	
Rise time	t_{R}		-	-	170	ns	
Turn-off delay time	$t_{\text{D}(\text{OFF})}$		-	-	140	ns	
Fall time	t_{F}		-	-	130	ns	
Total gate charge	Q_{G}	$V_{\text{DS}}=480\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=7.4\text{A}$ (note 4,5)	-	29	38	nC	
Gate-source charge	Q_{GS}		-	7	-	nC	
Gate-drain charge	Q_{GD}		-	14.5	-	nC	
Drain-source diode characteristics							
Drain-source diode forward voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{SD}}=7.4\text{A}$	-	-	1.4	V	
Continuous drain-source current	I_{SD}		-	-	7.4	A	
Pulsed drain-source current	I_{SM}		-	-	29.6	A	
Reverse recovery time	t_{RR}	$I_{\text{SD}}=7.4\text{A}, dI_{\text{SD}}/dt=100\text{A}/\mu\text{s}$ (note 4)	-	320	-	ns	
Reverse recovery charge	Q_{RR}		-	2.4	-	μC	

Note: 1. Repetitive rating: pulse width limited by maximum junction temperature

2. $L=19.5\text{mH}, I_{\text{AS}}=7.4\text{A}, V_{\text{DD}}=50\text{V}, R_{\text{G}}=25\Omega$, starting $T_J=25^\circ\text{C}$ 3. $I_{\text{SD}} \leq 7.4\text{A}, dI/dt \leq 200\text{A}/\mu\text{s}, V_{\text{DD}} \leq \text{BV}_{\text{DSS}}$, starting $T_J=25^\circ\text{C}$ 4. Pulse test: pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$

5. Essentially independent of operating temperature

Test circuits and waveforms

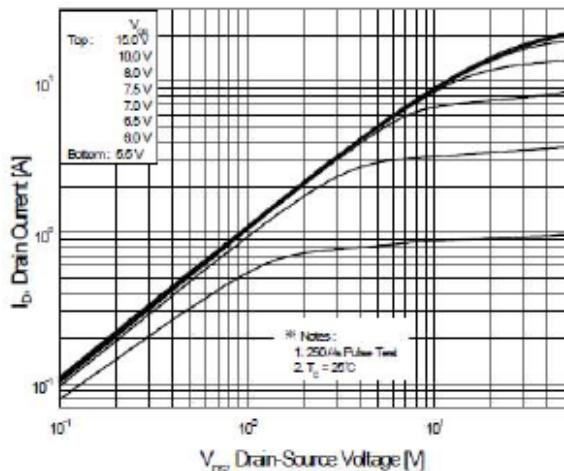


Figure 1. On-Region Characteristics

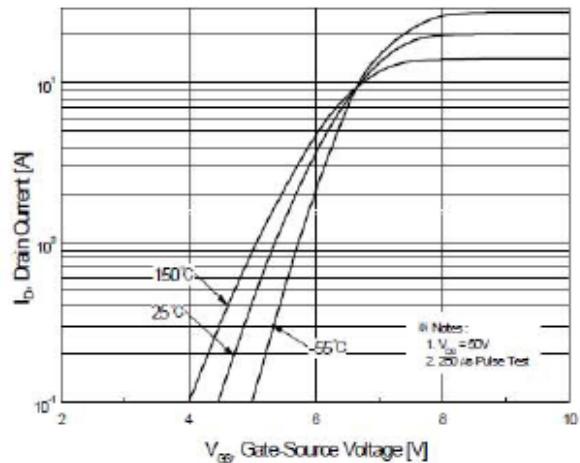


Figure 2. Transfer Characteristics

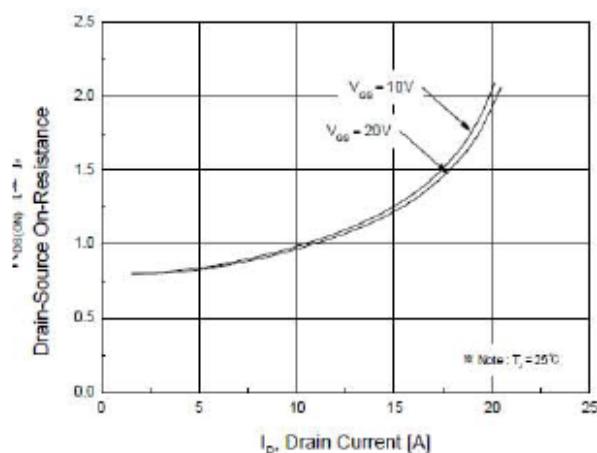


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

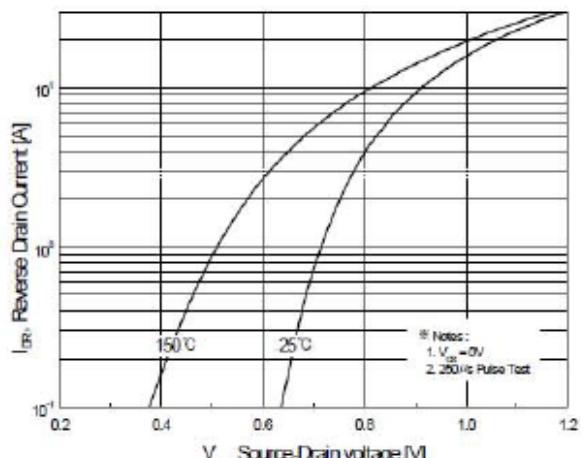


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

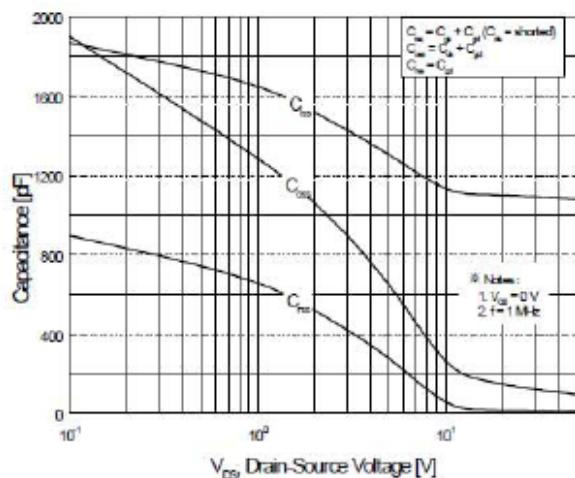


Figure 5. Capacitance Characteristics

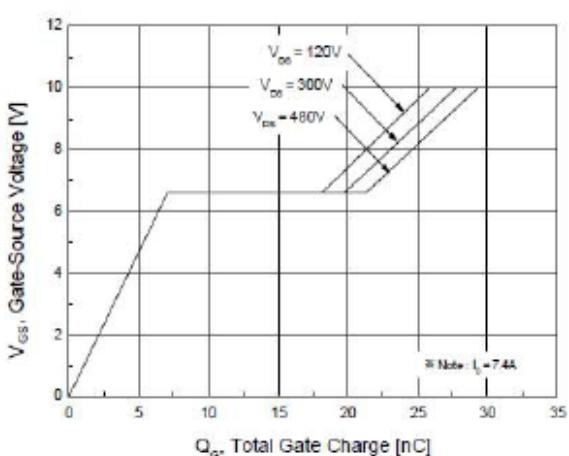


Figure 6. Gate Charge Characteristics

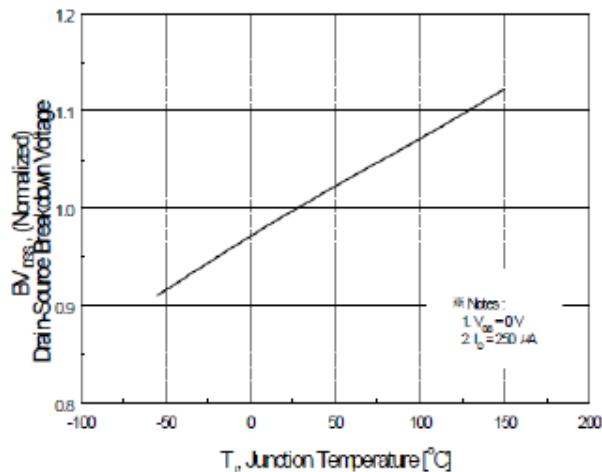


Figure 7. Breakdown Voltage Variation vs. Temperature

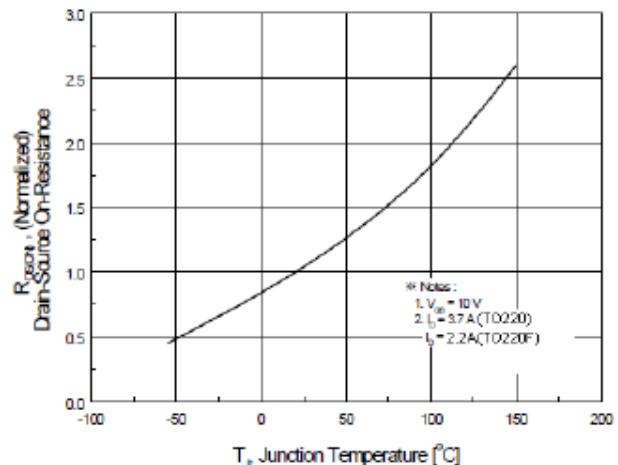


Figure 8. On-Resistance Variation vs. Temperature

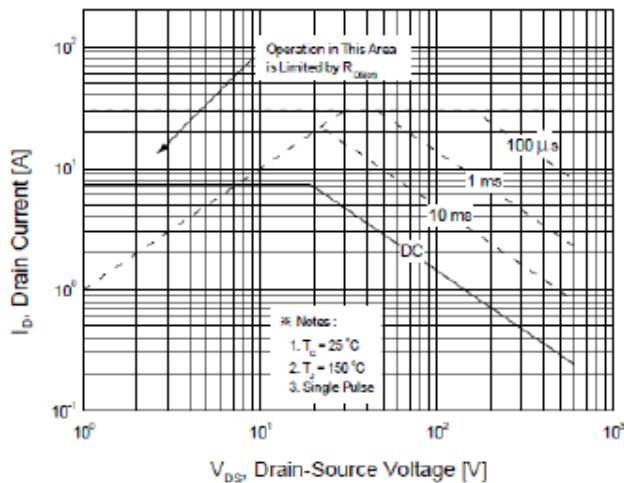


Figure 9 . Maximum Safe Operating Area

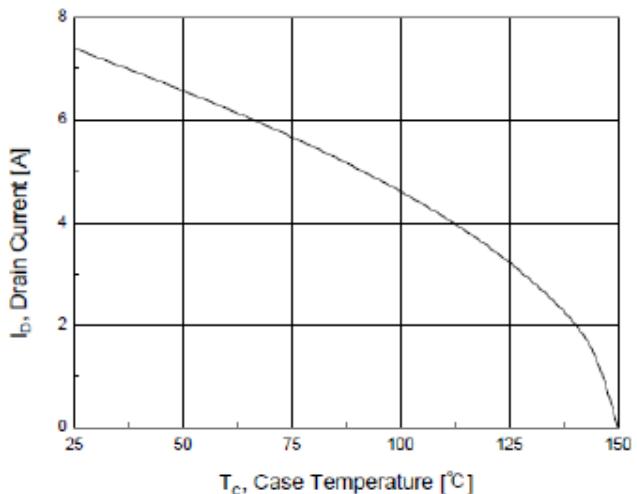


Figure 10. Maximum Drain Current vs Case Temperature

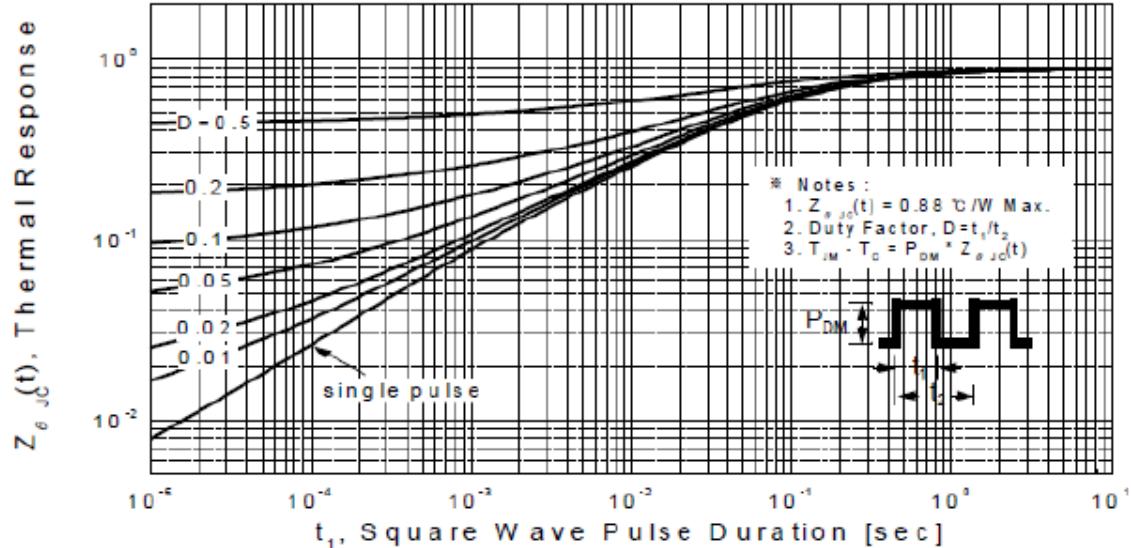


Figure 11. Transient Thermal Response Curve